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Identification

Catalog of GE 645 addressing reference faults D. R. Widrig

Introduction

Certain faults detected during address references are currently handled in a variety of ways by the GE 645 prototype processor. This document outlines all possible illegal addressing faults and describes the fault taken by the processor to indicate the error.

Table of GE 645 Fault Taken For Certain Addressing Faults

Illegal Action		Fault Taken
1.	Reference outside segment bound	Illegal procedure, Out-of-bounds subcondition
2.	Reference outside all existing port block number settings	Illegal procedure, Out-of-bounds subcondition
3.	Reference to a port with Port Enable OFF	Illegal procedure, Out-of-bounds subcondition
4.	Reference to a System Controller with port masked off	Operation not complete
5.	Reference to a port with Port Enable ON but nothing attached to port	Operation not complete
6.	Illegal command detected by attached prototype clock	Parity
7.	Illegal command detected by attached System Controller	Illegal Memory Command
8.	Illegal use of an addressing modifier	Operation not complete

Illegal actions 1, 6, and 8 may be generated by user programs. All other illegal actions reflect a mis-configuration of the system or a hardware difficulty.