MULTICS SYSTEM-PROGRAMMERS MANUAL

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Identification

Connect generator procedure Harlow Frick

Purpose

Connect_generator is a master mode procedure which issues connect signals to all processors in the system. Connect_ generator is accessible to those Multics modules which must clear the associative memories of all processors in the system. Each processor clears its associative memory in response to a connect fault as described in BK.3.08.

Discussion

For each processor available to the system, connect_generator performs the following steps:

- 1. Sets connect_flag for the target processor to non-zero. (See Section BK.4.01 for a description of the connect_flag array.)
- 2. Issues a connect signal to the target processor.
- 3. Waits for a positive response from the target processor. When the target processor detects the connect signal, it will respond by clearing its entry in the connect_flag array.

(In the implementation of connect_generator, Steps 1 and 2 are repeated for every processor available to the system before Step 3 is executed.)

Connect_generator is called as follows:

call master_mode_ut\$connect_generator;